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PATENT NUMBER and  
ISSUE DATE

## U.S. UTILITY Patent Application

APPL. NUM	FILING DATE	CLASS	SUBCLASS	GAI	EXAMINER
10020407	12/12/2001	438	14	2822 2842	COTHORN Trish

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\*\*CONTINUING DATA VERIFIED:

\*\*FOR (GN) APPLICATIONS VERIFIED:

PG-PUB. DO NOT PUBLISH <input checked="" type="checkbox"/>	RESCIND <input type="checkbox"/>	ATTORNEY DOCKET NO
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no	<input type="checkbox"/> yes <input type="checkbox"/> no	01-299 71730
35 USC 119(e) conditions met <input type="checkbox"/> yes <input type="checkbox"/> no	Voided and Acknowledged Examiner's initials	
TITLE: A method of detecting spatially correlated variations in a parameter of an integrated circuit die		
US DEPT. OF COMM. PAT. & T.M. FTO-4304Rv. (2-94)		

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
		DRAWING	
		Sheets Drwg.	Figs. Drwg.
Assistant Examiner		Print Fig.	
ISSUE FEE		Primary Examiner	
Amount Due	Date Paid	PREPARED FOR ISSUE	
TERMINAL		Application Examiner	
DISCLAIMER			
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